

(12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(19) World Intellectual Property  
Organization  
International Bureau



(43) International Publication Date  
19 May 2005 (19.05.2005)

PCT

(10) International Publication Number  
WO 2005/046217 A3

(51) International Patent Classification<sup>7</sup>: H04N 3/15 [NL/NL]; Groenewoudseweg 1, NL-5621 BA Eindhoven (NL).

(21) International Application Number: PCT/IB2004/052298 (72) Inventors; and  
(75) Inventors/Applicants (for US only): NASCETTI, Augusto [IT/DE]; c/o Philips Intellectual Property & Standards GmbH, Weisshausstr. 2, 52066 Aachen (DE). RÜTTEN, Walter [DE/DE]; c/o Philips Intellectual Property & Standards GmbH, Weisshausstr. 2, 52066 Aachen (DE).

(22) International Filing Date: 4 November 2004 (04.11.2004) (74) Agents: VOLMER, Georg et al.; Philips Intellectual Property & Standards GmbH, Weisshausstr. 2, 52066 Aachen (DE).

(25) Filing Language: English (81) Designated States (unless otherwise indicated, for every kind of national protection available): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BW, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NA, NI, NO, NZ, OM, PG,

(26) Publication Language: English

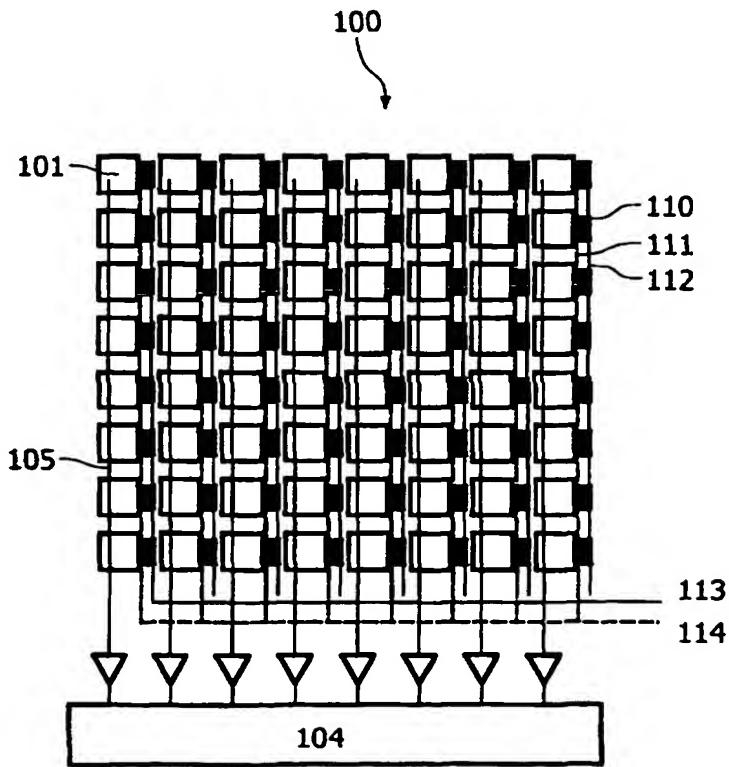
(30) Priority Data: 03104144.5 11 November 2003 (11.11.2003) EP

(71) Applicant (for DE only): PHILIPS INTELLECTUAL PROPERTY & STANDARDS GMBH [DE/DE]; Stein-damm 94, 20099 Hamburg (DE).

(71) Applicant (for all designated States except DE, US): KONINKLIJKE PHILIPS ELECTRONICS N. V.

[Continued on next page]

(54) Title: CIRCUIT FOR ADDRESSING ELECTRONIC UNITS



(57) Abstract: The invention relates to an addressing circuit for an array arrangement (100) of electronic units (101), which may be, for example, pixels of an X-ray detector. Every pixel (101) is connected to a spatially adjacent shift register (110), the shift registers (110) being connected in turn column-wise in series and also being connected to a common clock line (111,114). A trigger signal fed via an external trigger line (113) is passed by the shift registers (110) from row to row for every clock signal on the clock lines (111,114). In this process, triggered shift registers (110) activate the associate pixels (101) so that they can be read out via read-out lines (105) that extend column-wise.

WO 2005/046217 A3



PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM, ZW.

(84) **Designated States (unless otherwise indicated, for every kind of regional protection available):** ARIPO (BW, GH, GM, KE, LS, MW, MZ, NA, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IS, IT, LU, MC, NL, PL, PT, RO, SE, SI, SK, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

**Published:**

- *with international search report*
- *before the expiration of the time limit for amending the claims and to be republished in the event of receipt of amendments*

(88) **Date of publication of the international search report:**  
14 July 2005

*For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.*